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(54) **INTEGRATED CIRCUIT MODULE FOR INFORMATION SECURITY**

INTEGRIERTES SCHALTUNGSMODUL FÜR INFORMATIONSSICHERHEIT

MODULE DE CIRCUIT INTÉGRÉ POUR LA SÉCURITÉ D'INFORMATIONS

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EP 4 086 800 B1

Description

FIELD OF THE INVENTION

[0001] The present invention relates to an integrated circuit module for information security, and more particularly to an integrated circuit module for information security in data storage and communication applications.

BACKGROUND OF THE INVENTION

[0002] With the popularization of information technologies, it is a common need for people to properly store digital information. However, a lot of important information, e.g., various accounts, passwords and confidential information, which is really required to keep confidential, are stored in information processing devices of users, such as common personal computers, notebook computers, or even more popular smartphones, together with ordinary information. Therefore, if the important information to be kept confidential are not properly stored, there is a high risk of being hacked and causing significant damage. In today's usage scenario, smartphones and similar portable information processing devices frequently use various data transmission channels, e.g., Universal Serial Bus (USB) interface, Bluetooth, or wireless network, to exchange data or conduct financial transactions with other information devices or servers on the cloud. Therefore, there is a need to encrypt and decrypt the important data sent or received. However, few information processing devices in the hands of users today have such functions, and only a few newly launched information processing devices may have data security modules built in to accomplish the function of secure data storage. Moreover, most of the existing information processing devices cannot have data encryption/decryption functions through simple installation. Even if some information processing devices such as the computing device disclosed in US 2019/0188397 A1 may perform encryption/decryption for data at rest, encryption/decryption cannot be performed for data at transit.

SUMMARY OF THE INVENTION

[0003] The main purpose to develop technical means of the present invention is to solve the problems caused by the conventional means. The present invention principally relates to an integrated circuit module for information security, which includes: a secure circuit unit, which has passed a security evaluation as a cryptographic module and stores therein at least one digital key for providing a digital key service; and a controller unit set which is in communication with the secure circuit unit and includes a fast service unit and a trusted zone unit. The trusted zone unit and the secure circuit unit respectively use a first channel establishment key and a second channel establishment key dependent on each other to establish a secure signal channel. The secure circuit unit transmits a specific data to the fast service unit via the security signal channel to perform a fast service.

[0004] Based on the above idea, in the IC module for information security according to the present invention, the first channel establishment key and the second channel establishment key are derived from the digital key.

[0005] Based on the above idea, in the IC module for information security according to the present invention, the trusted zone unit contains a volatile memory. After the trusted zone unit uses a main key to perform an initialization process with the digital key provided by the secure circuit unit, a set of derived data is obtained from the digital key and the main key and stored back to the secure circuit unit. In the subsequent procedure of establishing the secure signal channel, the secure circuit unit sends the set of derived data to the trusted zone unit, and the trusted zone unit uses the main key and the set of derived data to restore the digital key and stores it in a volatile memory. After the trusted zone unit completes mutual verification with the secure circuit unit by way of the digital key, the trusted zone unit and the secure circuit unit use the digital key to derive the first channel establishment key and the second channel establishment key respectively. The secure signal channel, by way of the first channel establishment key and the second channel establishment key, has the specific data transmitted therevia under encryption. The specific data obtained by the trusted zone unit is stored into the volatile memory. After the controller unit set is powered off, the digital key stored in the volatile memory will disappear, and the process of establishing the secure signal channel will be restarted once the controller unit is powered on.

[0006] Based on the above idea, in the IC module for information security according to the present invention, the first channel establishment key and the second channel establishment key are session keys of the same contents. The secure signal channel, by way of the session keys, has the specific data transmitted therevia under encryption. The specific data contains a key for fast service for the fast service unit to perform the fast service.

[0007] Based on the above idea, in the IC module for information security according to the present invention, the mutual verification includes the following steps. The trusted zone unit issues a first challenge to the security circuit unit, which is a generated random number, and sends it to the security circuit unit. In response to the first challenge, the security circuit unit sends back a first response to the trusted zone unit, which is the random number encrypted with the digital key. The trusted zone unit then decrypts the first response based on the digital key to get back the decrypted random number, thereby determining whether the decrypted random number is the same as the generated random number. The security circuit unit issues a second challenge to the trusted zone unit, which is another generated random number, and sends it to the trusted zone unit. In response to the

second challenge, the trusted zone unit sends back a second response to the security circuit unit, which is the another random number encrypted with the digital key. The security circuit unit then decrypts the second response according to the digital key to get back the decrypted another random number, thereby determining whether the decrypted another random number is the same as the generated another random number. The mutual verification is then completed.

[0008] Based on the above idea, in the IC module for information security according to the present invention, the trusted zone unit uses a secure technology of Physically Unclonable Function (PUF) to generate the main key.

[0009] Based on the above idea, in the IC module for information security according to the present invention, the trusted zone unit obtains a first public key pairing the digital key in the initialization process. A second public key of a second public-private key pair in the trusted zone unit is sent to the secure circuit unit, and stored in the secure circuit unit. The trusted zone unit uses a PUF secure technology to generate the main key, and uses the main key to conduct encryption protection of the second public-private key pair. The trusted zone unit uses a key establishment process to derive a share key from the first public key and a second private key of the second public-private key pair. The secure circuit unit uses the key establishment process to derive the share key from the second public key and the digital key. The secure circuit unit and the trusted zone unit then use a session key derived from the shared key to serve as the first channel establishment key and the second channel establishment key.

[0010] Based on the above idea, in the IC module for information security according to the present invention, the secure circuit unit is a secure integrated circuit chip, and the controller unit set is a controller integrated circuit chipset.

[0011] Based on the above idea, in the IC module for information security according to the present invention, the fast service unit is implemented with an AI chip, a field programmable gate array unit, or an application specific integrated circuit (ASIC) for conducting the fast service, which is a fast service via a changeable interface.

[0012] Another aspect of the present invention is a memory module exhibiting an information security function, which includes the above described integrated circuit module for information security and the following devices, including a non-volatile memory for storing digital information; and an interface controller in communication with the non-volatile memory, the integrated circuit module for information security, and a host for conducting digital data transmission between the non-volatile memory and the host, and using the fast service provided by the integrated circuit module for information security.

[0013] A further aspect of the present invention is a memory module exhibiting an information security function, which includes the above-described integrated circuit module for information security and a non-volatile memory for storing digital information. The integrated circuit module for information security is in communication with the non-volatile memory and a host for conducting digital data transmission between the non-volatile memory and the host, and providing the fast service.

[0014] Still another aspect of the present invention is a hardware module exhibiting an information security function, which includes the above-described integrated circuit module for information security and a communication controller. The communication controller is in communication with a host for conducting digital data transmission between the hardware module and the host, and providing the fast service.

[0015] Based on the above idea, in the hardware module exhibiting an information security function according to the present invention, the communication controller is a network interface controller. The integrated circuit module for information security is further coupled to a network module. The network module is used for communicating with the external internet or mobile network. Data information transmitted between the host and a user device at the other end of the internet or mobile network are encrypted/decrypted by way of the fast service unit in the integrated circuit module for information security.

[0016] Based on the above idea, in the hardware module exhibiting an information security function according to the present invention, the integrated circuit module for information security and the communication controller are integrated in a housing to form the hardware module exhibiting information security and encryption/decryption functions. The integrated circuit module for information security conducts digital data transmission with a non-volatile memory in the host via the communication controller. The host transmits unencrypted information in the non-volatile memory to the fast service unit in the integrated circuit module for information security to be fast encrypted. The encrypted information is then transmitted back to the non-volatile memory in the host via the communication controller to be stored. The host transmits the encrypted information to the fast service unit in the integrated circuit module for information security to be fast decrypted. The decrypted information is then transmitted back to the non-volatile memory in the host via the communication controller to be stored.

[0017] In order to have a clear understanding of the above ideas of the present invention, a number of embodiments are given below with corresponding drawings as detailed below.

BRIEF DESCRIPTION OF THE DRAWINGS

[0018]

FIG. 1 is a functional block diagram of an integrated circuit module for information security according to a preferred embodiment of the present invention;

FIG. 2A is a flowchart illustrating a process of establishing a secure signal channel according to the present invention;

FIG. 2B is a functional block diagram and data exchange scheme illustrating establishment of a secure signal channel by way of another key establishment mechanism that is not encompassed by the wording of the claims but is considered as useful for understanding the invention; and

FIGS. 3A, 3B and 3C are functional block diagrams exemplifying three applications of an integrated circuit module for information security according to the present invention in different hardware environments.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

[0019] In order to solve the above-mentioned shortcomings of the conventional means, the inventor of the present invention has developed a preferred embodiment of an integrated circuit module 1 for information security, as shown in the schematic functional block diagram of FIG. 1. The IC module 1 for information security according to the present invention mainly includes a security circuit unit 10 and a controller unit set 11. The secure circuit unit 10 is a circuit unit that has passed the secure module verification, e.g., the verifying test of Common Criteria Evaluation Assurance Level 5+, or short named "EAL5+"), and a storage unit 100 therein stores at least one digital key 1001 and ordinary digital information 1002. The digital key 1001 is used for conducting digital key services. The digital key services may be functions of cryptographic services, key management, data storage, and so on. The secure circuit unit 10 may further include an encryption unit 102 for providing necessary encryption/decryption services. Its processing speed, however, is not fast enough. Therefore, a fast service unit 111 is additionally developed according to the present invention to make improvement.

[0020] Thus, the controller unit set 11 developed according to the present invention mainly includes the fast service unit 111 and a trusted zone unit 112. The trusted zone unit 112 and the secure circuit unit 10 respectively use a first channel establishment key and a second channel establishment key, which are dependent on each other, to establish a secure signal channel 101. The secure circuit unit 10 uses the secure signal channel 101 to transmit a specific data to the fast service unit 111. The fast service unit 111 uses the specific data to conduct, with a host 2, a fast service 1110 via a changeable interface. The fast service unit 111 may be implemented with an artificial intelligence (AI) chip, a field programmable gate array (FPGA) unit, or other types of application specific integrated circuit (ASIC) for conducting a fast service 1110 via a changeable interface with the host 2. The specific data, for example, may include a key for fast service. The key for fast service is originally well preserved in the secure circuit unit 10. Examples of the key for fast service include an identity key indicating a specific identity, e.g., a client of an online bank, a cryptocurrency account password, or a key for encrypting a confidential document. Accordingly, the fast service 1110 may be a fast log-in operation, information encryption/decryption such as AES encryption/decryption, and digital signature production and signature verification, by way of the key for fast service. The fast service unit 111 is disposed with a specific hardware circuit for fast information encryption/decryption, e.g., AES encryption/decryption and digital signature production and signature verification, for promoting the encryption/decryption processing speed of the entire IC module 1 for information security. Furthermore, by way of the following technical means, reliability of the information security of the IC module 1 can be extended to the fast service unit 111. The dependent first channel establishment key and second channel establishment key have a specific relationship. The simplest one is that both are identical, or there is a function/mapping relationship therebetween.

[0021] A process of establishing the above-described secure signal channel 101 is exemplified by further referring to the flowchart shown in FIG. 2A. First of all, the trusted zone unit 112 in the controller unit set 11 uses a main key to conduct an initialization process of the digital key provided by the secure circuit unit 10 during a packaging process of the above-described IC module 1 for information security or when the system is reset to factory settings. The initialization process mainly aims to obtain a set of derived data, and the trusted zone unit 112 stores the set of derived data back to the secure circuit unit 10 (Step 21). The set of derived data is derived and obtained by operating the main key and the digital key. For example, it is a set of data generated after an encryption operation of the digital key with the main key. Furthermore, after the trusted zone unit 112 stores the set of derived data back to the secure circuit unit 10, it will not store the set of derived data in a complete form, or it will delete the set of derived data. The object is that the set of derived data will not be simultaneously stored in the trusted zone unit 112 and the secure circuit unit 10 in a complete form until a secure channel is established. The way of generating the main key by the above-described trusted zone unit 112 may be implemented with the secure technology of Physically Unclonable Function, which is hereinafter so called as PUF. Due to the features of being random, unique, and unable to reproduce, the generated main key plays a chip-fingerprint like role. Therefore, the main key can inherently differ with the chip circuit feature of the trusted zone unit 112, and it is not easy to hack away the main key.

[0022] As such, when it is necessary to establish the secure signal channel 101, the secure circuit unit 10 sends the set of derived data back to the trusted zone unit 112 again (Step 22). Accordingly, the trusted zone unit 112 gets back the digital key according to the main key and the set of derived data again, and stores it into a

volatile memory 1120 (Step 23). Then the trusted zone unit 112 can use the digital key to conduct mutual verification with the secure circuit unit 10 (Step 24). The trusted zone unit 112 and the secure circuit unit 10, which finish the mutual verification, respectively use the digital key to derive the first channel establishment key and the second channel establishment key, thereby successfully establishing the secure signal channel 101. It can be performed by way of a symmetric-encryption algorithm. As such, the first channel establishment key and the second channel establishment key may be session keys of the same contents (Step 25). Then the secure signal channel 101 can use the session keys to conduct encrypted transmission of the specific data (Step 26). Just as the above example, the specific data may include the key for fast service. The key for fast service is stored in the volatile memory 1120. After the controller unit set 11 is powered off, the digital key stored in the volatile memory 1120 and the key for fast service will disappear (Step 27). In the meantime, the key for fast service is stored only in the better protected secure circuit unit 10. In response to regeneration of the need for another fast service, the process of establishing the secure signal channel 101 can be started again after the controller unit set 11 is powered on again (Step 28).

[0023] As for an exemplified process of the above-mentioned mutual verification may include the following steps. A first challenge to the secure circuit unit 10 is issued by the trusted zone unit 112, e.g., the trusted zone unit 112 generates a random number and transmits it to the secure circuit unit 10. The secure circuit unit 10 sends a first response, which is the random number encrypted with the digital key, to the trusted zone unit 112. The trusted zone unit 112 decrypts the first response according to the digital key to get back the random number. Then, whether the random number is the random number initially issued can be determined, thereby performing verification. A second challenge to the trusted zone unit 112 is issued by the secure circuit unit 10, e.g., the secure circuit unit 10 generates another random number and transmits it to the trusted zone unit 112. The second challenge, for example, is another random number generated by the secure circuit unit 10. The trusted zone unit 112 sends a second response to respond to the secure circuit unit 10, which is the another random number encrypted with the digital key, to the secure circuit unit 10. The secure circuit unit 10 decrypts the second response with the digital key to get back the another random number. Then, whether the another random number is the random number initially issued can be determined, thereby performing mutual verification. Of course, the orders of the above-described two challenges may be exchanged.

[0024] Furthermore, other embodiments not encompassed by the wording of the claims but considered as useful for understanding the invention may also use other key establishment mechanism to establish the secure signal channel 101. For example, as shown by the functional blocks and data exchange scheme in FIG. 2B, this embodiment is characterized in that even in the initialization process during manufacturing, the digital key in the secure circuit unit 10 will not be outputted to the external, but only public keys are exchanged. The trusted zone unit 112 can obtain a first public key, which pairs the digital key. Meanwhile, a second public key of a second public-private key pair in the trusted zone unit 112 is sent to the secure circuit unit 10, and stored in the secure circuit unit 10. For securing the second public-private key pair from being hacked, the trusted zone unit 112 may use the PUF-based main key to encrypt the second public-private key pair. Subsequently, a share key is generated by the trusted zone unit 112 according to a key establishment process. The key establishment algorithm, for example, can be Elliptic Curve Diffie-Hellman key exchange (ECDH), by which the share key is derived from the first public key and a second private key of the second public-private key pair. The secure circuit unit 10 may also use the same key establishment process, e.g., ECDH, and derive the share key from the second public key and the digital key. The secure circuit unit 10 and the trusted zone unit 112 then use the share key to derive a session key, which serves as each of the first channel establishment key and the second channel establishment key. The first channel establishment key and the second channel establishment key are session keys of the same contents, and the secure signal channel 101 uses the session keys to conduct symmetric-encryption transmission, e.g., AES encryption. The session keys will disappear as the controller unit set 11 is powered off. Subsequently, when the controller unit set 11 is powered on, the process of establishing the secure signal channel 101 restarts again. The above-described ECDH process may be a type of Elliptic Curve Diffie-Hellman key exchange(2s), which is ECDH(2s) in short. That is, two static keys are used for generating a share key. In other words, the resulting share key will not change, and it is necessary to derive different session keys each time to avoid from cracking. Alternatively, different types of key establishment ways, e.g., ECDH(2s,1e) or ECDH(2s,2e), may also be introduced. By introducing an ephemeral key, different share keys can be generated each time without the derivation operation of the session key, and security of the system can be further improved. Since the above-described ECDH(2s,1e) and ECDH(2s,2e) have been developed to be well known algorithms, they are not redundantly described herein.

[0025] As for the secure circuit unit 10, it may be implemented with a commercially available secure element, e.g., SLE 97 produced by Infineon Technologies. The controller unit set 11 may be implemented with an integrated chipset consisting of a single chip or composite chips. The fast service unit 111 may be implemented with an artificial intelligence (AI) chip, a field programmable gate array (FPGA) unit, or other application specific integrated circuit (ASIC) for conducting the fast service 1110 via a changeable interface with the host.

In this way, the IC module 1 for information security made by the technical means according to the present invention can exhibit the information security function. Hereinafter, a variety of applications developed with the IC module 1 for information security serving as a core element are introduced. Examples of the above-described fast service 1110 via a changeable interface can be referred to the following descriptions of exemplified applications.

[0026] Please refer to FIGS. 3A, 3B and 3C, which illustrate the use of the IC module 1 for information security according to the present invention in different hardware environments. As shown in FIG. 3A, the IC module 1 for information security is in communication with a flash memory 30 via an interface controller 31, which are further integrated into a memory module exhibiting an information security function. The flash memory 30 can be used for storing digital information. For example, if the interface controller 31 is a controller of Secure Digital Memory Card, i.e., SD card controller. Then those shown in FIG. 3A will be able to constitute an SD card or a microSD card exhibiting an information security function. When the SD card or microSD card exhibiting an information security function is inserted into an ordinary card-reader. For example, the host 2 as shown is a computer or a smartphone, and a memory card-reader is coupled thereto. It may be transformed into a USB dongle exhibiting an information security function. For example, if the IC module 1 for information security according to the present invention has passed the verification executed by the fast identity online (FIDO) appliance, the card-reader inserted with the SD card or microSD card exhibiting an information security function can be transformed into a USB dongle exhibiting an information security function, or a security token. The flash memory 30, of course, may be implemented with other types of non-volatile memory, and not to be redundantly described herein.

[0027] In this way, the consumer can transform an ordinary card reader into a USB dongle exhibiting an information security function, or a security token, by purchasing the SD card or microSD card exhibiting an information security function, which is developed according to the present invention. By way of easy installation, information encryption/decryption functions can be achieved. Furthermore, the interface controller 31 may also be directly implemented with a USB controller. Accordingly, the circuit blocks shown in FIG. 3A may be integrated into one housing to directly form a USB dongle exhibiting an information security function, and communicable with the host 2 via the I/O interface 310, i.e., the USB interface herein. Likewise, if the IC module 1 for information security according to the present invention has passed the verification executed by the fast identity online (FIDO) appliance, a USB token passing the verification executed by the fast identity online (FIDO) appliance can be obtained with the IC module 1 for information security serving as a core and integrated therewith an ordinary USB interface controller.

[0028] After the I/O interface 310, e.g., the above-described microSD card interface or a USB interface, of the interface controller 31 accomplish communication with the host 2, the host 2 can communicate with the IC module 1 for information security and the flash memory 30 by way of a microSD card protocol or a USB protocol. Furthermore, the data stored in the flash memory 30 can have the encryption service from the IC module 1 for information security.

[0029] As shown in FIG. 3B, an interface controller 1110 is integrated into the fast service unit 111. Accordingly, the flash memory 30 is in communication with the host 2 via an I/O interface 320 provided by the fast service unit 111 of the IC module for information security. Installation of the interface controller 31 shown in FIG. 3A can thus be omitted. For example, in consideration of simplicity of material preparation, the fast service unit 111 may be implemented with an FPGA unit. By way of programming settings to adjust functions so as to accomplish the interface control functions of a USB, a peripheral component interconnect express (PCIe), an integrated drive electronics (IDE), a serial advanced technology attachment (SATA), or a small computer system interface (SCSI). The flash memory 30 may further be integrated with the IC module 1 for information security in the same housing, thereby directly constituting a portable secure SSD exhibiting an information security function. Of course, the fast service unit 111 may also be implemented with an ASIC, which is lower in cost and poorer in function.

[0030] As shown in FIG. 3C, it is a hardware module exhibiting an information security function with the IC module 1 for information security serving as a core. The fast service unit 111 in the IC module 1 for information security is communicable with the host 2 via an I/O interface 330 directly provided by a communication controller 33, which is implemented with an ASIC, an FPGA unit or a system on a chip (SoC). The communication controller 33 may be a network interface card controller, and the IC module 1 for information security may further be connected to a network module 39 for communication with an external internet (not shown) or mobile network (not shown). Accordingly, data information transmitted between the host 2 and a user device (not shown) at the other end of the internet or mobile network are encrypted/decrypted by way of the IC module 1 for information security. With the installation of the fast service unit 111, performance can be further improved. In this way, what is shown in FIG. 3C can directly constitute a network communication device exhibiting an information security function, e.g., a voice over internet protocol (VoIP) device exhibiting information security and encryption/decryption functions, or a virtual private network (VPN) device exhibiting information security and encryption/decryption functions.

[0031] Of course, the communication controller 33 may also be implemented with a PCI-E bus controller, and

may also be a USB, IDE, SATA or SCSI interface controller. Accordingly, as shown in FIG. 3C, the integrated IC module 1 for information security and communication controller 33 in one housing (not shown), a hardware security module (HSM) exhibiting information security and encryption/decryption functions can be obtained. Then it is not necessary to install the network module 39 in the HSM. Since the IC module 1 for information security can communicate with the non-volatile memory 20 in the host 2 for digital data transmission via the communication controller 33, the unencrypted data stored in the non-volatile memory 20 can be transmitted by the host to the fast service unit 111 in the IC module 1 via the communication controller 33 to be encrypted. The encrypted data is then transmitted back to the non-volatile memory 20 in the host 2 via the communication controller 33 to be stored. On the other hand, the host 2 sends the encrypted data stored in the non-volatile memory 20 to the fast service unit 111 in the IC module 1 for information security via the communication controller 33 to be decrypted. The decrypted data is then transmitted back to the non-volatile memory 20 in the host 2 via the communication controller 33 to be stored or for other uses, e.g., shown on a display. In this way, the host 2 can be transformed into a network attached storage (NAS) exhibiting an information security function. The fast service as described above can be used to conduct fast encryption or decryption of the transmitted or received data by using various session keys derived from the digital key properly stored in the IC module 1 for information security, or to perform fast digital signature production and signature verification.

[0032] In summary, although the present invention is disclosed by way of embodiments as described above, it is not intended to limit the present invention. The invention may be modified and embellished to the extent that it is within the scope of the technology of the invention. Therefore, the scope of protection of the present invention shall be subject to the scope of the patent application as defined in the attached claims.

DESCRIPTION OF LABELLING

[0033]

1: integrated circuit module for information security
10: secure circuit unit
11: controller unit set
100: storage unit
1001: digital key
1002: digital information
102: encryption unit
111: fast service unit
112: trusted zone unit
101: secure signal channel
1110: fast service via changeable interface
1120: volatile memory
30: flash memory
31: interface controller
310: I/O interface
2: host
320: I/O interface
33: communication controller
330: I/O interface
39: network module

Claims

1. An integrated circuit module (1) for information security, comprising:

a secure circuit unit (10), having passed a security evaluation as a cryptographic module (102) and storing therein at least one digital key (1001) for providing a digital key service; and

a controller unit set (11) in communication with the secure circuit unit (10), including a fast service unit (111) and a trusted zone unit (112), wherein the trusted zone unit (112) and the secure circuit unit (10) respectively use a first channel establishment key and a second channel establishment key, which are dependent on each other, to establish a secure signal channel (101), and the secure circuit unit (10) transmits a specific data to the fast service unit (111) via the secure signal channel (101) to perform a fast service,

wherein:

the first channel establishment key and the second channel establishment key are derived from the at least one digital key (1001), the trusted zone unit (112) includes a volatile memory (1120),

after the trusted zone unit (112) uses a main key to perform an initialization process with the digital key (1001) provided by the secure circuit unit (10), and after a set of derived data is obtained from the at least one digital key (1001) and the main key and stored back to the secure circuit unit (10), the secure circuit unit (10) sends the set of derived data to the trusted zone unit (112) in a subsequent process of establishing the secure signal channel (101), and the trusted zone unit (112) uses the main key and the set of derived data to restore the at least one digital key (1001) and stores it in the volatile memory (1120);

after the trusted zone unit (112) completes mutual verification with the secure circuit unit (10) by way of the at least one digital key (1001), the trusted zone unit (112) and the secure circuit unit (10) use the digital key

to derive the first channel establishment key and the second channel establishment key respectively;
the secure signal channel (101), by way of the first channel establishment key and the second channel establishment key, has the specific data transmitted therevia under encryption,
the specific data obtained by the trusted zone unit (112) is stored into the volatile memory (1120), and
after the controller unit set (11) is powered off, the digital key stored in the volatile memory (1120) will disappear, and the process of establishing the secure signal channel (101) will be restarted once the controller unit set (11) is powered on.

2. The integrated circuit module (1) for information security according to claim 1, wherein the first channel establishment key and the second channel establishment key are session keys of the same contents; the secure signal channel (101), by way of the session keys, has the specific data transmitted therevia under encryption; and the specific data contains a key for fast service for the fast service unit (111) to perform the fast service.

3. The integrated circuit module (1) for information security according to claim 1, wherein the mutual verification includes steps of: the trusted zone unit (112) issuing a first challenge to the security circuit unit (10), which is a generated random number, and sending it to the security circuit unit (10); in response to the first challenge, the security circuit unit (10) sending back a first response to the trusted zone unit (112), which is the random number encrypted with the at least one digital key (1001); the trusted zone unit (112) decrypting the first response based on the at least one digital key (1001) to get back the decrypted random number, thereby determining whether the decrypted random number is the same as the generated random number; the security circuit unit (10) issuing a second challenge to the trusted zone unit (112), which is another generated random number, and sending it to the trusted zone unit (112); in response to the second challenge, the trusted zone unit (112) sending back a second response to the security circuit unit (10), which is the another random number encrypted with the at least one digital key (1001); and the security circuit unit (10) decrypting the second response according to the at least one digital key (1001) to get back the decrypted another random number, thereby determining whether the decrypted another random number is the same as the generated another random number, thereby completing the mutual verification.

4. The integrated circuit module (1) for information security according to claim 1, wherein the trusted zone unit (112) uses a secure technology of Physically Unclonable Function (PUF) to generate the main key.

5. The integrated circuit module (1) for information security according to claim 1, wherein the secure circuit unit (10) is a secure integrated circuit chip, and the controller unit set (11) is a controller integrated circuit chipset.

6. The integrated circuit module (1) for information security according to claim 1, wherein the fast service unit (111) is implemented with an AI chip, a field programmable gate array unit, or an application specific integrated circuit (ASIC) for conducting the fast service, which is a fast service via a changeable interface (1110).

7. A memory module exhibiting an information security function, **characterized in** comprising the integrated circuit module (1) for information security as recited in claim 1 and the following devices:

a non-volatile memory (30) for storing digital information; and
an interface controller (1110) in communication with the non-volatile memory (30), the integrated circuit module (1) for information security, and a host (2) for conducting digital data transmission between the non-volatile memory (30) and the host (2), and using the fast service provided by the integrated circuit module (1) for information security.

8. A memory module exhibiting an information security function, **characterized in** comprising: the integrated circuit module (1) for information security as recited in claim 1, and a non-volatile memory (30) for storing digital information, wherein the integrated circuit module (1) for information security is in communication with the non-volatile memory (30) and a host (2) for conducting digital data transmission between the non-volatile memory (30) and the host (2), and providing the fast service.

9. A hardware module exhibiting an information security function, **characterized in** comprising: the integrated circuit module (1) for information security as recited in claim 1, and a communication controller (33), wherein the communication controller is in communication with a host (2) for conducting digital data transmission between the hardware module and the host (2), and providing the fast service.

10. The hardware module exhibiting an information security function according to claim 10, wherein the

communication controller (33) is a network interface controller; the integrated circuit module (1) for information security is further coupled to a network module (39); the network module (39) is used for communicating with the external internet or mobile network; and data information transmitted between the host (2) and a user device at the other end of the internet or mobile network are encrypted/decrypted by way of the fast service unit (111) in the integrated circuit module (1) for information security.

11. The hardware module exhibiting an information security function according to claim 10, wherein the integrated circuit module (1) for information security and the communication controller (33) are integrated in a housing to form the hardware module exhibiting information security and encryption/decryption functions; the integrated circuit module (1) for information security conducts digital data transmission with a non-volatile memory (20) in the host (2) via the communication controller (33); the host (2) transmits unencrypted information in the non-volatile memory (20) to the fast service unit (111) in the integrated circuit module (1) for information security to be fast encrypted; the encrypted information is transmitted back to the non-volatile memory (20) in the host (2) via the communication controller (33) to be stored; the host (2) transmits the encrypted information to the fast service unit (111) in the integrated circuit module (1) for information security to be fast decrypted; and the decrypted information is transmitted back to the non-volatile memory (20) in the host (2) via the communication controller (33) to be stored.

Patentansprüche

1. Ein integriertes Schaltungsmodul (1) für Informationssicherheit, das Folgendes umfasst:

eine Sicherheitsschaltungseinheit (10), die eine Sicherheitsevaluation als ein kryptographisches Modul (102) bestanden hat und darin mindestens einen digitalen Schlüssel (1001) zur Bereitstellung eines digitalen Schlüsseldienstes speichert; und

einen Steuereinheitensatz (11) in Kommunikation mit der Sicherheitsschaltungseinheit (10), der eine Schnelldiensteinheit (111) und eine vertrauenswürdige-Zone-Einheit (112) umfasst, wobei die vertrauenswürdige-Zone-Einheit (112) und die Sicherheitsschaltungseinheit (10) jeweils einen ersten Kanalaufbauschlüssel und einen zweiten Kanalaufbauschlüssel verwenden, die voneinander abhängig sind, um einen sicheren Signalkanal (101) aufzubauen, und die Sicherheitsschaltungseinheit (10) spezifische Daten über den sicheren Signalkanal (101) an die Schnelldiensteinheit (111) sendet, um einen Schnelldienst durchzuführen,

wobei

der erste Kanalaufbauschlüssel und der zweite Kanalaufbauschlüssel von dem mindestens einen digitalen Schlüssel (1001) abgeleitet sind,

die vertrauenswürdige-Zone-Einheit (112) einen flüchtigen Speicher (1120) umfasst,

nachdem die vertrauenswürdige-Zone-Einheit (112)) einen Hauptschlüssel verwendet, um einen Initialisierungsprozess mit dem mindestens einen digitalen Schlüssel (1001) durchzuführen, der von der Sicherheitsschaltungseinheit (10) bereitgestellt wird, und nachdem ein Satz abgeleiteter Daten von dem mindestens einen digitalen Schlüssel (1001) und dem Hauptschlüssel erhalten und zurück zur Sicherheitsschaltungseinheit (10) gespeichert wird, die Sicherheitsschaltungseinheit (10) den Satz abgeleiteter Daten an die vertrauenswürdige-Zone-Einheit (112) in einem nachfolgenden Prozess des Aufbaus des sicheren Signalkanals (101) sendet, und die vertrauenswürdige-Zone-Einheit (112) den Hauptschlüssel und den Satz abgeleiteter Daten verwendet, um den mindestens einen digitalen Schlüssel (1001) wiederherzustellen, und ihn in dem flüchtigen Speicher (1120) speichert;

nachdem die vertrauenswürdige-Zone-Einheit (112) die gegenseitige Überprüfung mit der Sicherheitsschaltungseinheit (10) mittels des mindestens einen digitalen Schlüssels (1001) abgeschlossen hat, verwenden die vertrauenswürdige-Zone-Einheit (112) und die Sicherheitsschaltungseinheit (10) den digitalen Schlüssel, um den ersten Kanalaufbauschlüssel beziehungsweise den zweiten Kanalaufbauschlüssel abzuleiten;

der sichere Signalkanal (101) mittels des ersten Kanalaufbauschlüssels und des zweiten Kanalaufbauschlüssels die spezifischen Daten, die darüber übertragen werden, verschlüsselt hat,

die von der vertrauenswürdige-Zone-Einheit (112) erhaltenen spezifischen Daten in dem flüchtigen Speicher (1120) abgespeichert werden, und

nach dem Ausschalten des Steuereinheitensatzes (11) der in dem flüchtigen Speicher (1120) gespeicherte digitale Schlüssel verschwindet, und der Prozess des Aufbaus des sicheren Signalkanals (101) neu gestartet wird, sobald der Steuereinheitensatz (11) eingeschaltet wird.

2. Das integrierte Schaltungsmodul (1) für Informationssicherheit nach Anspruch 1, wobei der erste Kanalaufbauschlüssel und der zweite Kanalaufbauschlüssel Sitzungsschlüssel derselben Inhalte sind; der sichere Signalkanal (101) mittels der Sitzungs-

schlüssel die über ihn übertragenen spezifischen Daten verschlüsselt hat; und die spezifischen Daten einen Schlüssel für einen Schnelldienst für die Schnelldiensteinheit (111) enthalten, um den Schnelldienst durchzuführen.

3. Das integrierte Schaltungsmodul (1) für Informationssicherheit nach Anspruch 1, wobei die gegenseitige Verifizierung die folgenden Schritte umfasst: die vertrauenswürdige-Zone-Einheit (112) gibt eine erste Aufforderung an die Sicherheitsschaltungseinheit (10) aus, die eine generierte Zufallszahl ist, und sendet sie an die Sicherheitsschaltungseinheit (10); als Reaktion auf die erste Aufforderung sendet die Sicherheitsschaltungseinheit (10) eine erste Antwort an die vertrauenswürdige-Zone-Einheit (112) zurück, die die mit dem mindestens einen digitalen Schlüssel (1001) verschlüsselte Zufallszahl ist; die vertrauenswürdige-Zone-Einheit (112) entschlüsselt die erste Antwort auf der Grundlage des mindestens einen digitalen Schlüssels (1001), um die entschlüsselte Zufallszahl zurückzubekommen, wodurch bestimmt wird, ob die entschlüsselte Zufallszahl die gleiche ist wie die erzeugte Zufallszahl; die Sicherheitsschaltungseinheit (10) gibt eine zweite Aufforderung an die vertrauenswürdige-Zone-Einheit (112) aus, die eine andere erzeugte Zufallszahl ist, und sendet sie an die vertrauenswürdige-Zone-Einheit (112); als Antwort auf die zweite Aufforderung sendet die vertrauenswürdige-Zone-Einheit (112) eine zweite Antwort an die Sicherheitsschaltungseinheit (10) zurück, die die andere Zufallszahl ist, die mit dem mindestens einen digitalen Schlüssel (1001) verschlüsselt ist; und die Sicherheitsschaltungseinheit (10) entschlüsselt die zweite Antwort gemäß dem mindestens einen digitalen Schlüssel (1001), um die entschlüsselte andere Zufallszahl zurückzubekommen, wodurch bestimmt wird, ob die entschlüsselte andere Zufallszahl die gleiche ist wie die erzeugte andere Zufallszahl, wodurch die gegenseitige Überprüfung abgeschlossen wird.

4. Das integrierte Schaltungsmodul (1) für Informationssicherheit nach Anspruch 1, wobei die vertrauenswürdige-Zone-Einheit (112) eine sichere Technologie der Physically Unclonable Function (PUF) zur Erzeugung des Hauptschlüssels verwendet.

5. Das integrierte Schaltungsmodul (1) für Informationssicherheit nach Anspruch 1, wobei die Sicherheitsschaltungseinheit (10) ein sicherer integrierter Schaltungschip ist und der Steuereinheitensatz (11) ein Controller-integrierter Schaltkreis-Chipsatz ist.

6. Das integrierte Schaltungsmodul (1) für Informationssicherheit nach Anspruch 1, wobei die Schnelldiensteinheit (111) mit einem KI-Chip, einer feldprogrammierbaren Gate-Array-Einheit oder einer anwendungsspezifischen integrierten Schaltung (ASIC) zur Durchführung des Schnelldienstes implementiert ist, der ein Schnelldienst über eine veränderbare Schnittstelle (1110) ist.

7. Ein Speichermodul mit einer Informationssicherheitsfunktion, **dadurch gekennzeichnet, dass** es das integrierte Schaltungsmodul (1) für Informationssicherheit nach Anspruch 1 und die folgenden Vorrichtungen umfasst:

einen nichtflüchtigen Speicher (30) zum Speichern digitaler Informationen; und
eine Schnittstellensteuerung (1110), die mit dem nichtflüchtigen Speicher (30), dem integrierten Schaltungsmodul (1) für Informationssicherheit und einem Host (2) in Verbindung steht, um eine digitale Datenübertragung zwischen dem nichtflüchtigen Speicher (30) und dem Host (2) durchzuführen und den von dem integrierten Schaltungsmodul (1) für Informationssicherheit bereitgestellten Schnelldienst zu verwenden.

8. Ein Speichermodul mit einer Informationssicherheitsfunktion, **dadurch gekennzeichnet, dass es** umfasst: das integrierte Schaltungsmodul (1) für Informationssicherheit nach Anspruch 1 und einen nichtflüchtigen Speicher (30) zum Speichern digitaler Informationen, wobei das integrierte Schaltungsmodul (1) für Informationssicherheit mit dem nichtflüchtigen Speicher (30) und einem Host (2) in Verbindung steht, um eine digitale Datenübertragung zwischen dem nichtflüchtigen Speicher (30) und dem Host (2) durchzuführen und den Schnelldienst bereitzustellen.

9. Ein Hardwaremodul mit einer Informationssicherheitsfunktion, **dadurch gekennzeichnet, dass es** umfasst: das integrierte Schaltungsmodul (1) für Informationssicherheit nach Anspruch 1 und eine Kommunikationssteuerung (33), wobei die Kommunikationssteuerung (33) mit einem Host (2) in Verbindung steht, um eine digitale Datenübertragung zwischen dem Hardwaremodul und dem Host (2) durchzuführen und den Schnelldienst bereitzustellen.

10. Das Hardwaremodul mit einer Informationssicherheitsfunktion nach Anspruch 9, wobei die Kommunikationssteuerung (33) ein Netzwerkschnittstellencontroller ist; das integrierte Schaltkreismodul (1) für Informationssicherheit ferner mit einem Netzwerkmodul (39) gekoppelt ist; das Netzwerkmodul (39) für die Kommunikation mit dem externen Internet oder dem mobilen Netzwerk verwendet wird; und Dateninformationen, die zwischen dem Host (2) und einem Benutzergerät am anderen Ende des Inter-

nets oder des mobilen Netzwerks übertragen werden, mittels der Schnelldiensteinheit (111) in dem integrierten Schaltkreismodul (1) für Informationssicherheit verschlüsselt/entschlüsselt werden.

11. Das Hardwaremodul mit einer Informationssicherheitsfunktion nach Anspruch 9, wobei das integrierte Schaltkreismodul (1) für Informationssicherheit und die Kommunikationssteuerung (33) in einem Gehäuse integriert sind, um das Hardwaremodul mit Informationssicherheits- und Verschlüsselungs-/Entschlüsselungsfunktionen zu bilden; das integrierte Schaltkreismodul (1) für Informationssicherheit eine digitale Datenübertragung mit einem nichtflüchtigen Speicher (20) im Host (2) über die Kommunikationssteuerung (33) durchführt; der Host (2) unverschlüsselte Informationen in dem nichtflüchtigen Speicher (20) an die Schnelldiensteinheit (111) in dem integrierten Schaltkreismodul (1) für Informationssicherheit überträgt, um sie schnell zu verschlüsseln; die verschlüsselten Informationen über die Kommunikationssteuerung (33) zurück an den nichtflüchtigen Speicher (20) im Host (2) übertragen werden, um gespeichert zu werden; der Host (2) die verschlüsselten Informationen an die Schnelldiensteinheit (111) in dem integrierten Schaltungsmodul (1) überträgt, damit die Informationssicherheit schnell entschlüsselt werden kann; und die entschlüsselten Informationen über die Kommunikationssteuerung (33) zurück an den nichtflüchtigen Speicher (20) im Host (2) übertragen werden, um gespeichert zu werden.

Revendications

1. Un module de circuit intégré (1) pour la sécurité d'informations, comprenant :

une unité de circuit de sécurité (10), ayant passé une évaluation de sécurité en tant que module cryptographique (102) et stockant au moins une clé numérique (1001) pour fournir un service de clé numérique ; et
un ensemble d'unités de contrôle (11) en communication avec l'unité de circuit de sécurité (10), comprenant une unité de service rapide (111) et une unité de zone fiable (112), dans lequel l'unité de zone fiable (112) et l'unité de circuit de sécurité (10) utilisent respectivement une première clé d'établissement de canal et une deuxième clé d'établissement de canal, qui sont dépendantes l'une de l'autre, pour établir un canal de signal sécurisé (101), et l'unité de circuit de sécurité (10) transmet des données spécifiques à l'unité de service rapide (111) via le canal de signal sécurisé (101) pour effectuer un service rapide,
dans lequel
la première clé d'établissement de canal et la deuxième clé d'établissement de canal sont dérivées d'au moins une clé numérique (1001),
l'unité de zone fiable (112) comprend une mémoire volatile (1120),
après que l'unité de zone fiable (112) a utilisé une clé principale pour effectuer un processus d'initialisation avec la au moins une clé numérique (1001) fournie par l'unité de circuit de sécurité (10), et après qu'un ensemble de données dérivées a été obtenu à partir de la au moins une clé numérique (1001) et de la clé principale et stocké à nouveau dans l'unité de circuit de sécurité (10), l'unité de circuit de sécurité (10) envoie l'ensemble de données dérivées à l'unité de zone fiable (112) dans un processus ultérieur d'établissement du canal de signal sécurisé (101), et l'unité de zone fiable (112) utilise la clé principale et l'ensemble de données dérivées pour restaurer la au moins une clé numérique (1001) et la stocke dans la mémoire volatile (1120) ;
après que l'unité de zone fiable (112) a terminé la vérification mutuelle avec l'unité de circuit de sécurité (10) au moyen de la au moins une clé numérique (1001), l'unité de zone fiable (112) et l'unité de circuit de sécurité (10) utilisent la clé numérique pour dériver respectivement la première clé d'établissement de canal et la deuxième clé d'établissement de canal ;
le canal de signal sécurisé (101), à l'aide de la première clé d'établissement de canal et de la deuxième clé d'établissement de canal, transmet les données spécifiques par le biais d'un cryptage,
les données spécifiques obtenues par l'unité de zone fiable (112) sont stockées dans la mémoire volatile (1120), et
après la mise hors tension de l'ensemble d'unités de contrôle (11), la clé numérique stockée dans la mémoire volatile (1120) disparaît, et le processus d'établissement du canal de signal sécurisé (101) redémarre dès que l'ensemble d'unités de contrôle (11) est mis sous tension.

2. Le module de circuit intégré (1) pour la sécurité d'informations selon la revendication 1, dans lequel la première clé d'établissement de canal et la deuxième clé d'établissement de canal sont des clés de session de même contenu ; le canal de signal sécurisé (101), au moyen des clés de session, transmet les données spécifiques par cryptage ; et les données spécifiques contiennent une clé pour le service rapide permettant à l'unité de service rapide (111) d'effectuer le service rapide.

3. Le module de circuit intégré (1) pour la sécurité d'informations selon la revendication 1, dans lequel

la vérification mutuelle comprend les étapes suivantes : l'unité de zone fiable (112) émet un premier défi à l'unité de circuit de sécurité (10), qui est un nombre aléatoire généré, et l'envoie à l'unité de circuit de sécurité (10) ; en réponse au premier défi, l'unité de circuit de sécurité (10) renvoie une première réponse à l'unité de zone fiable (112), qui est le nombre aléatoire crypté avec au moins une clé numérique (1001) ; l'unité de zone fiable (112) décryptant la première réponse sur la base de la au moins une clé numérique (1001) pour récupérer le nombre aléatoire décrypté, déterminant ainsi si le nombre aléatoire décrypté est le même que le nombre aléatoire généré ; l'unité de circuit de sécurité (10) émet un deuxième défi à l'unité de zone fiable (112), qui est un autre nombre aléatoire généré, et l'envoie à l'unité de zone fiable (112) ; en réponse au deuxième défi, l'unité de zone fiable (112) renvoie une deuxième réponse à l'unité de circuit de sécurité (10), qui est l'autre nombre aléatoire crypté avec la au moins une clé numérique (1001) ; et l'unité de circuit de sécurité (10) décryptant la deuxième réponse selon la au moins une clé numérique (1001) pour récupérer l'autre nombre aléatoire décrypté, déterminant ainsi si l'autre nombre aléatoire décrypté est le même que l'autre nombre aléatoire généré, achevant ainsi la vérification mutuelle.

4. Le module de circuit intégré (1) pour la sécurité d'informations selon la revendication 1, dans lequel l'unité de zone fiable (112) utilise une technologie sécurisée de fonction physiquement inclonable (PUF) pour générer la clé principale.

5. Le module de circuit intégré (1) pour la sécurité d'informations selon la revendication 1, dans lequel l'unité de circuit de sécurité (10) est une puce à circuit intégré sécurisé, et l'ensemble d'unités de contrôle (11) est un chipset à circuit intégré de contrôleur.

6. Le module de circuit intégré (1) pour la sécurité d'informations selon la revendication 1, dans lequel l'unité de service rapide (111) est mise en œuvre avec une puce IA, une unité de réseau de portes programmables sur site ou un circuit intégré spécifique à une application (ASIC) pour effectuer le service rapide, qui est un service rapide via une interface modifiable (1110).

7. Un module de mémoire présentant une fonction de sécurité des informations, **caractérisé en ce qu'**il comprend le module de circuit intégré (1) pour la sécurité d'informations selon la revendication 1 et les dispositifs suivants :

une mémoire non volatile (30) pour stocker des informations numériques ; et
un contrôleur d'interface (1110) en communication avec la mémoire non volatile (30), le module de circuit intégré (1) pour la sécurité d'informations et un hôte (2) pour effectuer la transmission de données numériques entre la mémoire non volatile (30) et l'hôte (2), et utilisant le service rapide fourni par le module de circuit intégré (1) pour la sécurité d'informations.

8. Un module de mémoire présentant une fonction de sécurité des informations, **caractérisé en ce qu'**il comprend : le module de circuit intégré (1) pour la sécurité d'informations selon la revendication 1, et une mémoire non volatile (30) pour stocker des informations numériques, dans lequel le module de circuit intégré (1) pour la sécurité d'informations est en communication avec la mémoire non volatile (30) et un hôte (2) pour effectuer la transmission de données numériques entre la mémoire non volatile (30) et l'hôte (2), et fournissant le service rapide.

9. Un module matériel présentant une fonction de sécurité des informations, **caractérisé en ce qu'**il comprend : le module de circuit intégré (1) pour la sécurité d'informations selon la revendication 1, et un contrôleur de communication (33), dans lequel le contrôleur de communication est en communication avec un hôte (2) pour effectuer la transmission de données numériques entre le module matériel et l'hôte (2), et fournir le service rapide.

10. Le module matériel présentant une fonction de sécurité des informations selon la revendication 9, dans lequel le contrôleur de communication (33) est un contrôleur d'interface réseau ; le module de circuit intégré (1) pour la sécurité d'informations est en outre couplé à un module réseau (39) ; le module réseau (39) est utilisé pour communiquer avec l'Internet externe ou le réseau mobile ; et les informations de données transmises entre l'hôte (2) et un dispositif utilisateur à l'autre extrémité de l'Internet ou du réseau mobile sont cryptées/décryptées au moyen de l'unité de service rapide (111) dans le module de circuit intégré (1) pour la sécurité d'informations.

11. Le module matériel présentant une fonction de sécurité des informations selon la revendication 9, dans lequel le module de circuit intégré (1) pour la sécurité d'informations et le contrôleur de communication (33) sont intégrés dans un boîtier pour former le module matériel présentant des fonctions de sécurité des informations et de cryptage/décryptage ; le module de circuit intégré (1) pour la sécurité d'informations effectue une transmission de données numériques avec une mémoire non volatile (20) dans l'hôte (2) via le contrôleur de communication (33) ; l'hôte (2) transmet des informations non cryptées dans la mémoire non volatile (20) à l'unité

de service rapide (111) dans le module de circuit intégré (1) pour la sécurité d'informations afin qu'elles soient rapidement cryptées ; les informations cryptées sont retransmises à la mémoire non volatile (20) dans l'hôte (2) via le contrôleur de communication (33) afin d'être stockées ; l'hôte (2) transmet les informations cryptées à l'unité de service rapide (111) dans le module de circuit intégré (1) pour la sécurité d'informations soit rapidement décryptée ; et les informations décryptées sont retransmises à la mémoire non volatile (20) dans l'hôte (2) via le contrôleur de communication (33) pour être stockées.

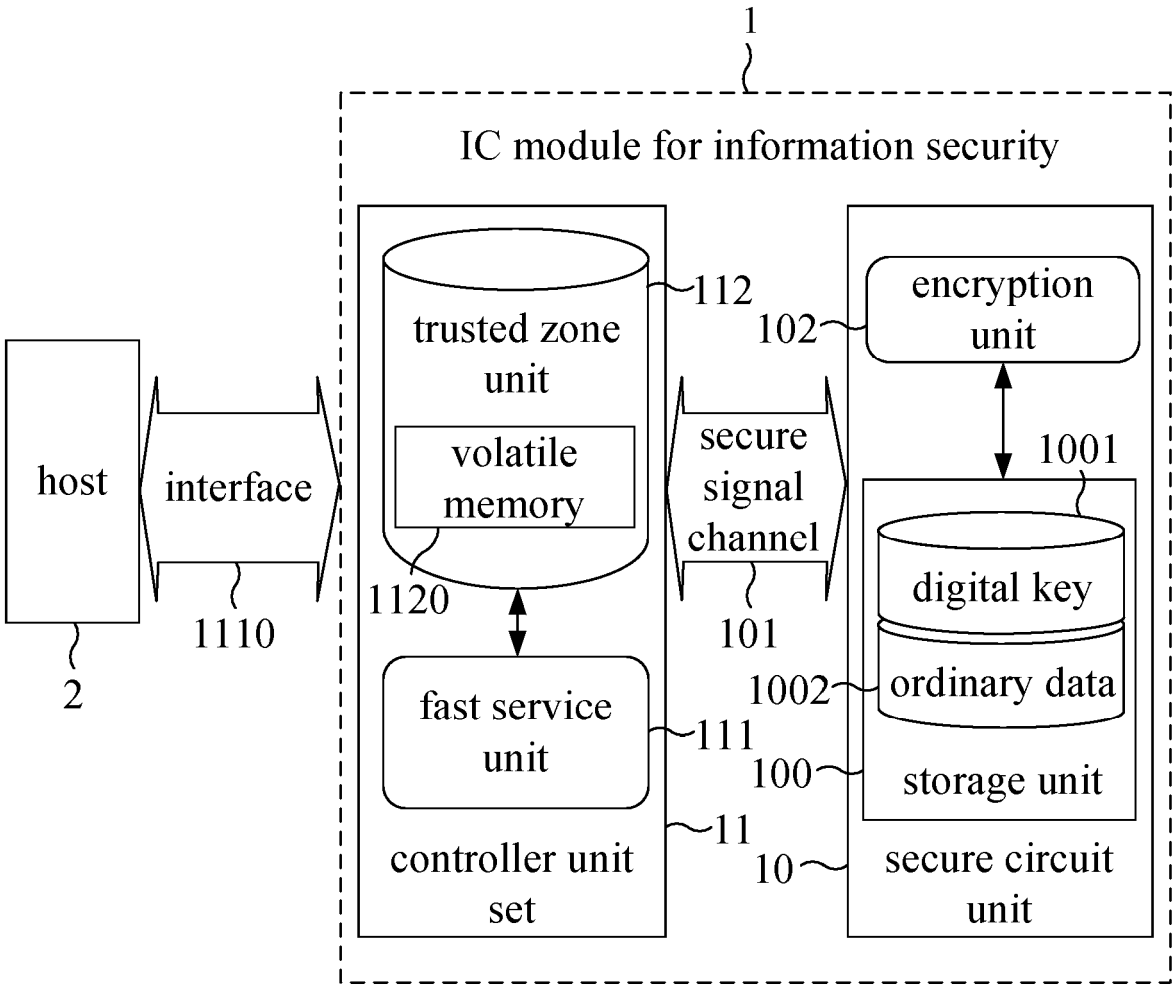
1
IC module for information security
trusted zone unit
112
volatile memory
1120
fast service unit
111
controller unit set
11
host
interface
1110
2
secure signal channel
101
encryption unit
102
1001
digital key
ordinary data
1002
storage unit
100
secure circuit unit
10

FIG. 1

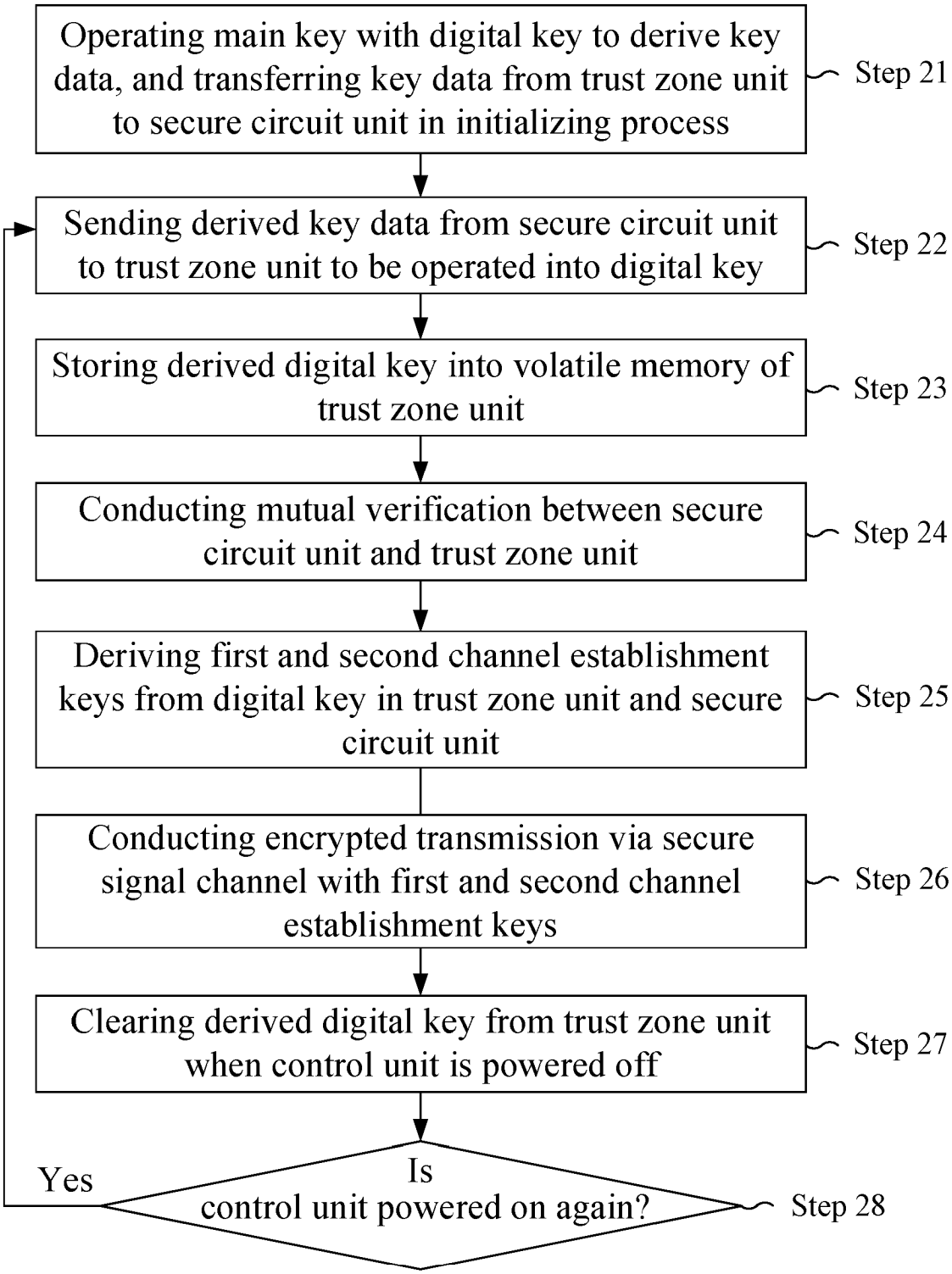
Operating main key with digital key to derive key data, and transferring key data from trust zone unit to secure circuit unit in initializing process
Step 21
Sending derived key data from secure circuit unit to trust zone unit to be operated into digital key
Step 22
Storing derived digital key into volatile memory of trust zone unit
Step 23
Conducting mutual verification between secure circuit unit and trust zone unit
Step 24
Deriving first and second channel establishment keys from digital key in trust zone unit and secure circuit unit
Step 25
Conducting encrypted transmission via secure signal channel with first and second channel establishment keys
Step 26
Clearing derived digital key from trust zone unit when control unit is powered off
Step 27
Is control unit powered on again?
Yes
Step 28

FIG. 2A

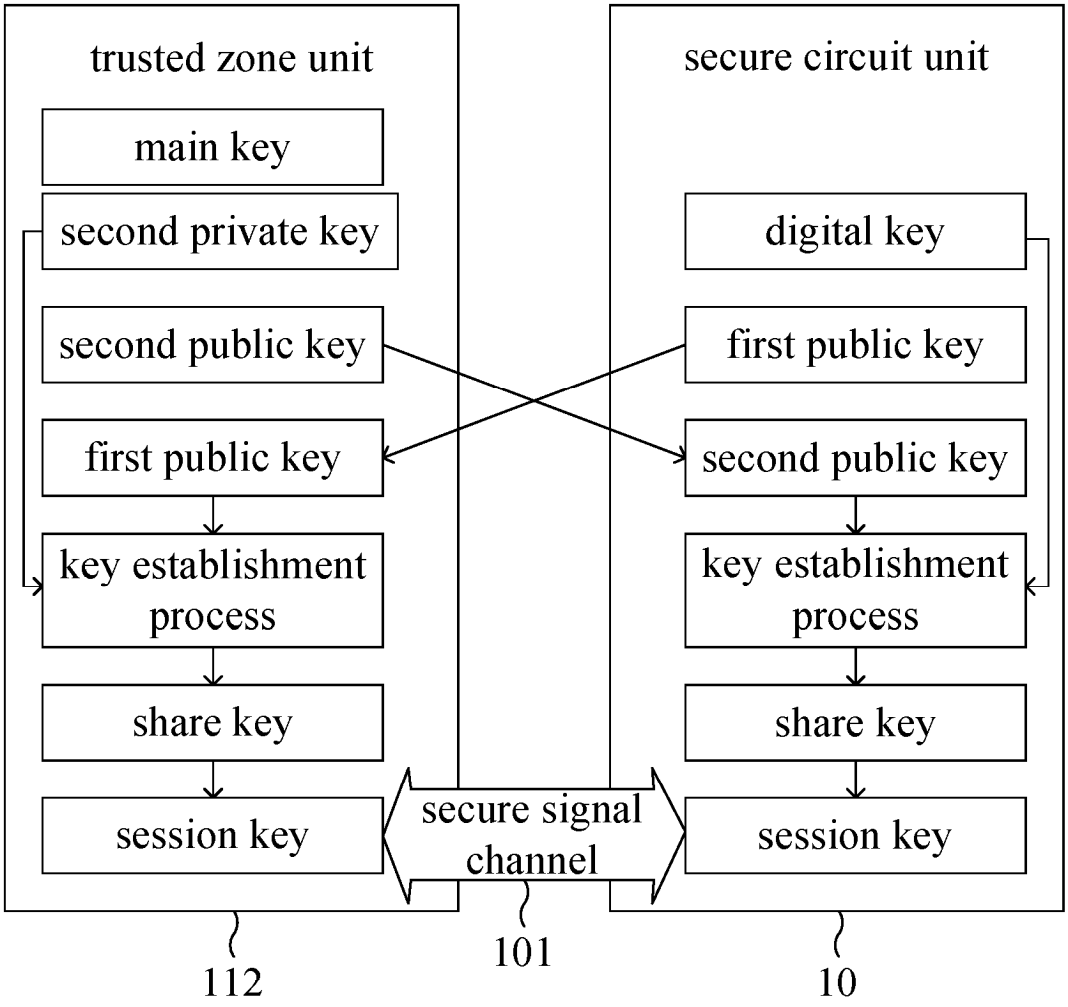
trusted zone unit
main key
second private key
second public key
first public key
key establishment process
share key
session key
secure circuit unit
digital key
first public key
second public key
key establishment process
share key
session key
secure signal channel
112
101
10

FIG. 2B

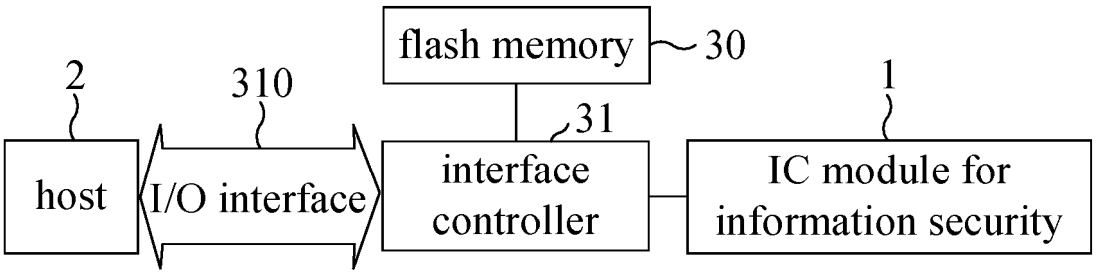

FIG. 3A

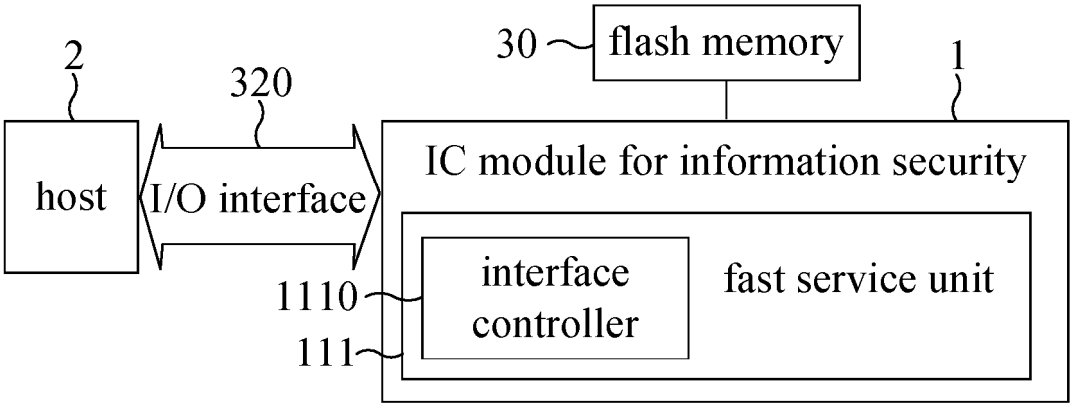

FIG. 3B

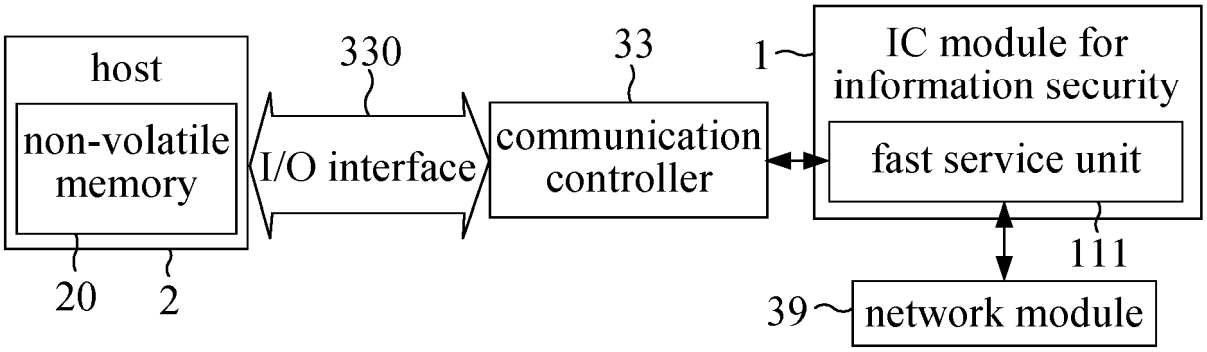

FIG. 3C

REFERENCES CITED IN THE DESCRIPTION

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Patent documents cited in the description

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